

Nokia

4A0-M05 Exam

Nokia Cloud Packet Core Exam



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Question: 1

Which of the following management tools performs the function of virtual infrastructure manager (VIM) in the CPC NFV architecture?

- A. NetAct
- B. CloudBand Infrastructure Software (CBIS)
- C. Operations Support System (OSS)
- D. Network Service Platform (NSP)

Answer: B

Question: 2

Which of the following statements about the network service platform (NSP) is FALSE?

- A. Acts as an element management system (EMS) for CPC components
- B. Performs fault, performance, and security management
- C. Has a northbound interface with the NFV orchestrator (NFVO)
- D. Allows monitoring events associated with the NFV environment

Answer: C

Question: 3

Which of the following network elements is implemented by the Nokia CPC?

- A. Policy and charging rules function (PCRF)
- B. Equipment Identity Registration (EIR)
- C. Application Function (AF)
- D. eNodeB

Answer: A

Question: 4

What is the main benefit of splitting the core network into separate user-plane and control-plane nodes?

- A. Independent configuration of user-plane and control-plane nodes

- B. Independent scaling of user-plane and control-plane nodes
- C. User of a shared data repository for storing user states.
- D. Creation of networks with isolated properties over a common infrastructure

Answer: B

Question: 5

Which of the following statements about SR-IOV is FALSE?

- A. VMs using OVS and SR-IOV can be instantiated on the same compute node.
- B. SR-IOV offers comparable performance to non-visualized hardware.
- C. The VM must be attached to the PF to achieve highest performance.
- D. Using SR-IOV restricts VM mobility.

Answer: D

Question: 6

Which of the following best describes the reason for CPU pinning?

- A. To ensure that a VM is assigned to high performance CPU cores.
- B. To ensure that a VM's CPU cores area all allocated on the same NUMA node.
- C. To ensure that a VM is assigned to a compute node with adequate resources.
- D. To ensure that a VM is assigned a sufficient number of CPU cores.

Answer: A

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